

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,369,098 B2
APPLICATION NO. : 10/598408
DATED : May 6, 2008
INVENTOR(S) : Zhining Chen

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the front page of the patent, in the Foreign Application Priority Data (30), the foreign priority application number should include "-9" and appears as follows:

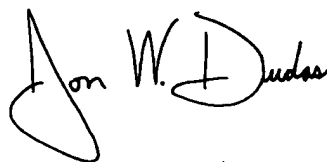
(30) Foreign Application Priority Data
Jan. 26, 2004 (SG)200400539-3

At the end of column 6, line 56, after claim 13, insert the following claims:

14. The method as in claim 13, comprising the step of providing each of the first and second array elements as a plate array element.
15. The method as in claim 14, comprising the step of providing each of the first and second ground conductors as a ground patch.
16. The method as in claim 15, comprising the step of providing the first ground patch as continuous with the second ground patch.

Signed and Sealed this

Fifth Day of August, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office